



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<u>Applicant(s):</u>	Masatoshi Ishikawa et al.	<u>Examiner:</u>	Seyed H. Azarian
<u>Application</u>	09/760,331	<u>Confirmation</u>	2692
<u>Filed:</u>	January 13, 2001	<u>Group Art</u>	2625
<u>For:</u>	IMAGE DETECTION PROCESSOR		

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Transmitted herewith is an Information Disclosure Statement pursuant to 37 C.F.R. §§ 1.97 and 1.98 in order to comply with the duty of disclosure under 37 C.F.R. § 1.56 being filed together with a Request for Continued Examination (RCE).

Copies of thirteen referenced documents, which are listed on the accompanying Form PTO/SB/08A, are enclosed herewith. A duplicate copy of Form PTO/SB/08A is transmitted herewith.

In accordance with 37 C.F.R. § 1.98, submission of this statement requires no fee. However, if for any reason a fee is due, the Commissioner is hereby authorized to charge payment of any fees required in connection with this Information Disclosure Statement to Deposit Account No. 50-0675, Order No. 876564.0038.

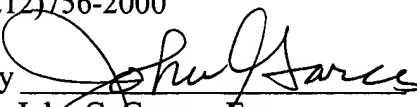
It is respectfully requested that these references be fully considered by the Patent and Trademark Office in response to the RCE and printed on any patent which may issue on this

application. Applicants request that a copy of Form PTO/SB/08A, as considered and initialed by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

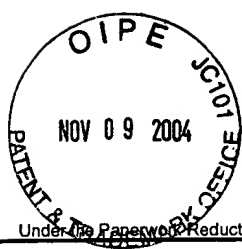
Respectfully submitted,

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Reg. No. 40,616

Dated: November 9, 2004
New York, New York

Encls.
JCG/DT



PTO/SB/08B (08-03)
Approved for use through 07/31/2006, OMB 0651-0031
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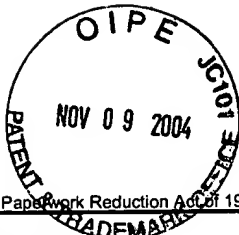
Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	09/760,331
		Filing Date	January 13, 2001
		First Named Inventor	Masatoshi Ishikawa
		Art Unit	2625
		Examiner Name	Seyed H. Azarian
Sheet 1	of 2	Attorney Docket Number	876564/0083

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Moment Extraction Architecture for Digital Vision Chip", Technical Report of IEICE, July 1997 (citing the following 12 references).	
		C. Mead and M. Mahowald, "A Silicon Model of Early Visual Processing", Neutral Networks, July 1988.	
		M. Ishikawa, A. Morita, and N. Takayanagi, "High Speed Vision System Using Massively Parallel Processing", IROS, 1992.	
		Y. Nakabo, I. Ishii, and M. Ishikawa, "1ms Target Tracking System Using Ultra-Parallel Ultra-High Speed Vision", 1997.	
		T. Oowaki, Y. Nakabo, A. Namiki, I. Ishii, and M. Ishikawa, "Real Time System for Virtually Touching Objects...", 1998 (no english abstract available).	
		A. Namiki, Y. Nakabo, I. Ishii, and M. Ishikawa, "Grasping System Using High Speed Vision Feedback", 1998.	
		I. Ishii, Y. Nakabo, and M. Ishikawa, "Target Tracking Algorithm for 1ms Visual Feedback System Using Massively Parallel Processing", IEEE, 1996.	
		T. Konuro, S. Suzuki, I. Ishii, and M. Ishikawa, "Design of Massively Parallel Vision Chip Using General-Purpose Processing Elements", 1998 (no english abstract available).	
		M. Ishikawa, K. Ogawa, T. Komuro, I. Ishii, "A CMOS Vision Chip with SIMD Processing Element Array for 1ms Image Processing", February 1999.	
		T. Bernard, B. Zavidovique, and F. Devos, "A Programmable Artificial Retina", IEEE Journal of Solid-State Circuits", July 1993.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		J. Eklund, C. Svensson, and A. Astrom, "VLSI Implementation of a Focal Plane Image Processor...", IEEE Trans. VLSI Systems, September 1996.	
		T. Mukai and M. Ishikawa, "Two-Dimensional Coordinates Transform Circuit for Parallel Processing Vision", Technical Report of IEICE, 1992.	
		I. Ishii, T. Komuro, and M. Ishikawa, "Moment Calculation Method using Bit-Plane Feature Decompensation", Technical Report of IEICE, July 1999.	

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